1553 Transfer Trigger Manual

Lahniss, September 2014

Contents

1	Int	troduction	1
2	Th	e User Interface concept of the Transfer Type Trigger	1
3	RH	IS tabs corresponding to each Transfer mode	2
4	Ex	Examples	
	4.1	Triggering on a RT to BC Transfer	3
	4.2	Triggering on a Mode Command	5

1 Introduction

This partial manual describes the MIL STD 1553 trigger in more details, in particular for the Transfer trigger. For all other trigger features, please refer to the Teledyne LeCroy avionics TD manual.

2 The User Interface concept of the Transfer Type Trigger

When using the Transfer trigger Mode, the possible trigger conditions are presented to the user according to the structure of the selected transfer. (It is assumed here that the reader is familiar with the Transfer types specified by MIL-STD-1553B.) This means that if a Transfer only has one type of words, only one tab will appear on the right. Conversely if a Transfer Type has a structure with a command word and a data word, 2 tabs will appear on the right, one to describe the trigger condition on the command word and another one to describe the condition on the data word.

The logical binding between the tabs is an AND condition. The trigger occurs when the condition described in tab 1 **AND** the condition in tab 2 are met on the same Transfer. This logic extends to the number of tabs shown.

3 RHS tabs corresponding to each Transfer mode

The following table lists the 10 Transfer Modes specified by MIL-STD-1553. Note that the First item in the list is "All" and does not correspond to a Transfer Mode. It is explained in the Table below

Index	Transfer Type	RHS tabs corresponding to the selected Transfer	Comments
0	"All",	All	Triggers on any Transfer type, without exercining conditions on the contents
1	"BC-RT (Rcv)",	RxCmd2 DW RxStat2	
2	"RT-BC (Xmit)",	TxCmd1 DW TxStat1	
3	"RT-RT",	TxCmd1 RxCmd2 DW TxStat1 RxStat2	
4	"Cmd",	TxCmd1 TxStat1	Mode command
5	"Cmd (Xmit)",	TxCmd1 DW TxStat1	Mode command with data from RT
6	"Cmd (Rcv)",	RxCmd2 DW RxStat2	Mode command with data to RT
7	"BC-RT(S)",	RxCmd2 DW	Broadcast
8	"RT-RT(S)",	TxCmd1 RxCmd2 DW TxStat1	Broadcast
9	"Cmd(S)",	TxCmd1	Broadcast
10	"Cmd(S)",	RxCmd2 DW	Broadcast

Figure 1: List of all Transfers as per MIL-STD-1553B and the corresponding trigger types

4 Examples

The following paragraphs show detailed examples for 2 selected cases of Transfer Mode trigger, a RT to BC Transfer as well as a Mode Command. All other modes operate in the same spirit. In both cases at least 2 variations of the same trigger are shown.



4.1 Triggering on a RT to BC Transfer

Figure 2: The trigger catches RT to BC Transfers from RT 4, containing DW=0006 at Bit Offset 32

There are several points worth noticing on this detailed example. The image above is a composite one, showing the contents of both relevant trigger tabs. Firstly, the tab set in the RHS section of the trigger dialog matches the constitution of the RT to BC Transfer: 1 TxCmd, 1 Data and 1 TxStatus. Here we have elected to trigger when RT address=4 AND DW in position 32 = 0006. This exact condition has been detected by the hardware trigger exactly where the yellow trigger arrow occurs. Note that the trigger arrow is placed chronologically exactly AFTER the occurrence of the DW=0006 condition. This reflects the fact that the trigger, if it occurs, can only be emitted after all of the conditions have been met.

What would happen when one and only one of the triggering condition was changed? This experiment is shown in the next page, with only the DW value changed from 0006 to 0002.

File Vertical Timebase Trigger Display Cursors Measure Math Analysis Utilities Help	Zoom 40
RT-BC (Xinit) MC Remote Terminal to Bus Controller RT-BC (Xinit) MC Remote Terminal to Bus Controller C S D D D D D D D D D D D D D D D D D D	
с	
Renote Termina to Bus Controller	
Xmit Command Status Word Data Word	Intermessage Gap = 65.79 µs
	interniessage oap = os./ a ps
Mill 1653 Time Type Summary Respo Data 1 -1.102 ms RT-800 (Ximt) RT 4 to B00 4 00W 5.27 µs 0x0000 0001 0002 0003	IMG Status
1 -1.102 ms RT-BC (Xinit) RT 4 to BC: 4 DW 5.27 μs 0x0000 0001 0002 0003 2 -315.10 μs Cnd RT 6: Override Xnitter Stutterdown 5.08 μs 3 -835.00 μs RT 4C DK: 10 W 5.33 μs 0x0004 0005 0006 0007 0004 0005 0006 0007 0008 0009 000e 0006 0006 0007 0010 0011 0012	MG Status U 65.78 µs u u u 39.02 µs u u u 201/7 mm u u u
1 -1.102 ms RT-BC (Xmit) RT 4 to BC, 4 DW 5.27 μs 0x0000 0001 0002 0003 2 -915.10 μs Cmd RT 6: Override Xmitter Studdown 5.08 μs 3 -855.00 μs RT-BC (Xmit) RT 4 to BC: 19 DW 5.33 μs 0x0000 0001 0002 0003 0006 0007 0004 0005 0006 0007 0000 0000 0000 0000 0000	65.78 µs 39.02 µs 311.7 µs 65.79 µs
1 -1.102 ms RT-BC (Xinit) RT 4 to BC: 4 DW 5.27 µs 0x0000 0001 0002 0003 2 -915,10 µs Cmd RT - BC override Xinitier Shutdown 5.08 µs 3 -835.00 µs RT - BC (Xinit) RT 4 to BC: 19 DW 5.33 µs 0x0000 0001 0002 0005 000s 0007 0004 0005 0006 0007 0008 0009 000s 0000 0000 0000 0000 0000	
1 -1.102 ms RT-BC (Xinit) RT 4 to BC: 4 DW 5.27 µs 0x0000 0001 0002 0003 2 -915,10 µs Cmd RT 6: Override Xmitter Studiown 5.08 µs 3 -835 00 µs RT-BC (Xinit) RT 4 to BC: 19 DW 5.33 µs 4 -102,10 µs RT 6: Override Xmitter Studiown 5.27 µs 0x0000 0001 0002 0003 0006 0007 0008 0009 000a 000b 000c 0000 0001 0011 0012 4 -102,10 µs RT-BC (Xinit) RT 4 to BC: 19 DW 5.33 µs 0x0000 0001 0002 0003 5 94.93 µs Cmd RT 6: Override Xmitter Studiown 5.08 µs 6 165.00 µs RT-BC (Xinit) RT 4 to BC: 19 DW 5.33 µs 6 165.00 µs RT-BC (Xinit) RT 4 to BC: 19 DW 5.33 µs 6 165.00 µs RT 4 to BC: 19 DW 5.33 µs 0x0004 0005 0006 0007 0004 0005 0006 0007 0008 0009 000a 000b 000c 0004 0006 0001 0011 0012 200 Vider 21 200 Vider 21 200 Vider	65.79 µs 39.02 µs Timebase 264 µs 200 µs/div Stop 0.00 V
1 -1.102 ms RT-BC (Xmt) RT 4 to BC: 4 DW 5.27 µs 0x0000 0001 0002 0003 2 -915.10 µs Cmd RT 6. Override Xmitter Studdown 5.08 µs 0x0000 0001 0002 0003 0006 0007 0008 0009 0006 0007 0008 0000 0001 0011 0012 3 -835 00 µs RT-BC (Xmt) RT 4 to BC: 10 VW 5.33 µs 0x0000 0001 0002 0003 4 -402.10 µs RT 8C (Xmt) RT 4 to BC: 10 VW 5.33 µs 0x0000 0001 0002 0003 5 84.93 µs Cmd RT 8. Override Xmitter Studdown 5.08 µs 0x0000 0001 0002 0003 5 84.93 µs Cmd RT 4 to BC: 19 DW 5.33 µs 0x0000 0001 0002 0003 5 84.93 µs Cmd RT 4 to BC: 19 DW 5.33 µs 0x0000 0001 0002 0003 0006 0007 0004 0005 0006 0007 0008 0009 000a 0000 0000 0000 0001 0011 0012 6 185 00 µs RT 4 to BC: 19 DW 5.33 µs 0x0000 0005 0006 0007 0004 0005 0006 0007 0008 0009 000a 0000 0000 0000 0000 0001 0011 001	65.79 µs 39.02 µs Timebase 264 µs 200 µs/div Stop 0.00 ∨ 2 MS 1 GS/s MIL1553
1 -1.102 ms RT 40 62: 4 DW 5.27 µs 0x0000 0001 0002 0003 2 -915.10 µs Coveride Xmtter Studiewn 5.06 µs 3 -835 00 µs RT-62 (Xmt) RT 4 to B2: 19 DW 5.33 µs 4 -102 10 µs RT-62 (Xmt) RT 4 to B2: 19 DW 5.33 µs 5 94 83 µs Cmd RT 6: Override Xmtter Studiewn 5.08 µs 5 94 83 µs Cmd RT 6: Override Xmtter Studiewn 5.08 µs 6 165 000 µs RT-8C (Xmt) RT 4 to B2: 19 DW 5.33 µs 0x0000 0001 0002 0003 5 94 83 µs Cmd RT 6: Override Xmtter Studiewn 5.08 µs 0x0000 0000 0001 0002 0003 6 165 00 µs RT-8C (Xmt) RT 4 to B2: 19 DW 5.33 µs 0x0000 0000 0000 0000 0000 0000 0000 0	65.79 µs 39.02 µs Timebase 264 µs 200 µs/div Stop 0.00 ∨ 2 MS 1 GS/s MIL1553
1 -1.102 ms RT 40 E (24 0W 5.27 µs 0x0000 0001 0002 0003 2 915.10 µs Cnd RT 6. Override Xmtter Studiown 5.08 µs 3 -835 00 µs RT-BC (Xmt) RT 4 to BC: 19 0V 5.33 µs 0x0000 0001 0002 0003 4 -102.10 µs RT 6. Override Xmtter Studiown 5.08 µs 0x0000 0001 0002 0003 5 94 x83 µs Cmd RT 6. Override Xmtter Studiown 5.08 µs 5 94 x83 µs Cmd RT 6. Override Xmtter Studiown 5.08 µs 6 165 00 µs RT-BC (Xmt) RT 4 to BC: 19 DW 5.33 µs 0x0000 0001 0002 0003 2 2.00 Vidity Z1 2.00 Vidity Z1 2.00 Vidity Z1 2.00 Vidity 2 2.00 Vidity Z1	65.79 µs 39.02 µs Timebase 264 µs 200 µs/div Stop 0.00 ∨ 2 MS 1 GS/s MIL1553
1 -1102 ms RT-BC (Kmt) RT 4 to BC: 4 DW 5.27 µs 0x0000 0001 0002 0003 2 -91510 µs Cmd RT 6. Override Xmitter Stutdown 5.08 µs 0x0000 0001 0002 0003 0006 0007 0004 0005 000	65.79 µs 39.02 µs Timebase 264 µs 200 µs/div Stop 0.00 ∨ 2 MS 1 GS/s MIL1553

Figure 3: The trigger catches RT to BC Transfers from RT 4, containing DW=0002 at Bit Offset 32

The image only shows the DW tab, in which the Data Value has been decreased from 0006 to 002. The impact on the trigger point is visible in the image. The trigger still occurs on the same Transfer Type (RT to BC) for the same RT Address (RT=4) and at the same Bit Offset (32) but for a DW Value of 0002. The trigger arrow is now observed AFTER DW = 0002, and lies in fact in another Transfer (the zoom Z1 is positioned differently on C2 then it was in the first image)

4.2 Triggering on a Mode Command

The following examples are shown without comments, based on the logic exposed previously.



Figure 1: The Trigger catches the Mode Command "Override Transmitter Shutdown", regardless of the Status Word Value. This is the fourth Transfer in the stream captured on C2



Figure 2: The Trigger catches the Mode Command "Dynamic Bus Control", regardless of the Status Word Value. This is the third Transfer in the stream captured on C2